## BEST AVAILABLE CO U.S. UTILITY Patent Application

PATENT NUMBER and **ISSUE DATE** 

CLAIMS ALLOWED

**EXAMINER** CLASS SUBCLASS GAU APPL NUM **FILING DATE** 10076450 02/19/2002 257 2815 Meister Ramdani Jamal; Droopad Ravindranath; Hilt Lyndee; Eisenbeiser Kurt; \*APPLICANTS: \*\*CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/502,023 02/10/2000 \*\* FOREIGN APPLICATIONS VERIFIED: RESCIND -PG-PUB DO NOT PUBLISH 🖵 ATTORNEY DOCKET NO Foreign priority claimed 🗆 yes 🗎 no 🗆 yes 🗅 no 35 USC 119 conditions met 219625US99DIV Verified and Acknowledged Examiners's intials TITLE: Semiconductor structure, semiconductor device, communicating device, integrated circuit, and process for fabricating the same U.S.DEPT, OF COMM./PAT.& TM-PTO-436L(Rev. 12-94

NOTICE OF ALLOWANCE MAILED	CLAIMS ALLOWED			YED:	
	Assistant Examiner	Total Claims		Print Claim for O.G	
ISSUE FEE			DRAWING		
Amount Due Date Paid	1	Sheets Drwg.	Figs.Drwg.	Print Fig.	
	Primary Examiner		<u> </u>		
TERMINAL	PREPARED FOR ISSUE	Application Examiner			
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